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Woodward, D.R.; Levy, D.C.; Harley, R.G.

Computer Design: VLSI in Computers and Processors, 1994. ICCD '94. Proceedings., IEEE International Conference on , 1994

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2 Peripheral component interconnect (PCI) interface with the QuickLogic QL16/spl times/24B FPGA

Geber, C.; Yee, K.

WESCON/94. Idea/Microelectronics. Conference Record , 1994

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3 Network interface for message-passing parallel computation on a workstation cluster

Hoe, J.C.

Hot Interconnects II, 1994. Symposium Record , 1994

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4 A family of user-programmable peripherals with a functional unit architecture

Shubat, A.S.; Trinh, C.Q.; Zaliznyak, A.; Ziklik, A.; Roy, A.; Kazerounian, R.; Cedar, Y Eitan, B.

Solid-State Circuits, IEEE Journal of , Volume: 27 Issue: 4 , April 1992

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5 Peripheral circuit design for field programmable MCM systems

Dobbelaere, I.; El Gamal, A.; How, D.; Kleveland, B.

Multi-Chip Module Conference, 1992. MCMC-92, Proceedings 1992 IEEE , 1992

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6 A large scale FPGA with 10 K core cells with CMOS 0.8 μ m 3-layered meta process

Muroga, H.; Murata, H.; Saeki, Y.; Hibi, T.; Ohashi, Y.; Noguchi, T.; Nishimura, T.
Custom Integrated Circuits Conference, 1991., Proceedings of the IEEE 1991, 1991
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